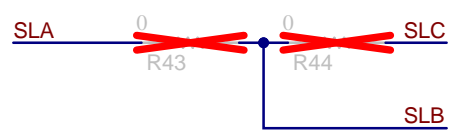
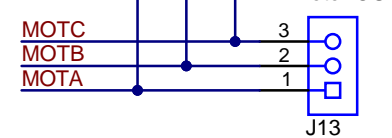
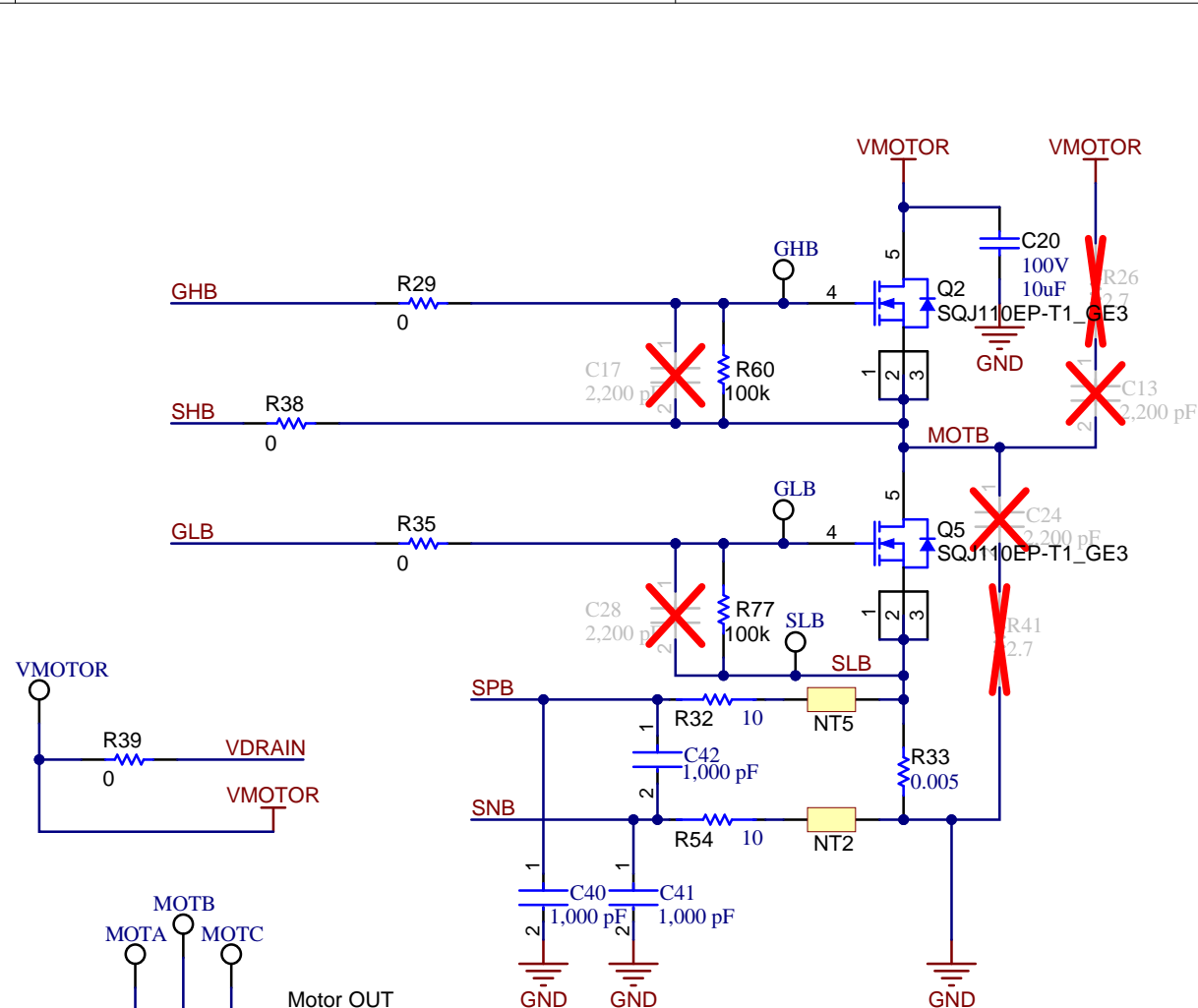
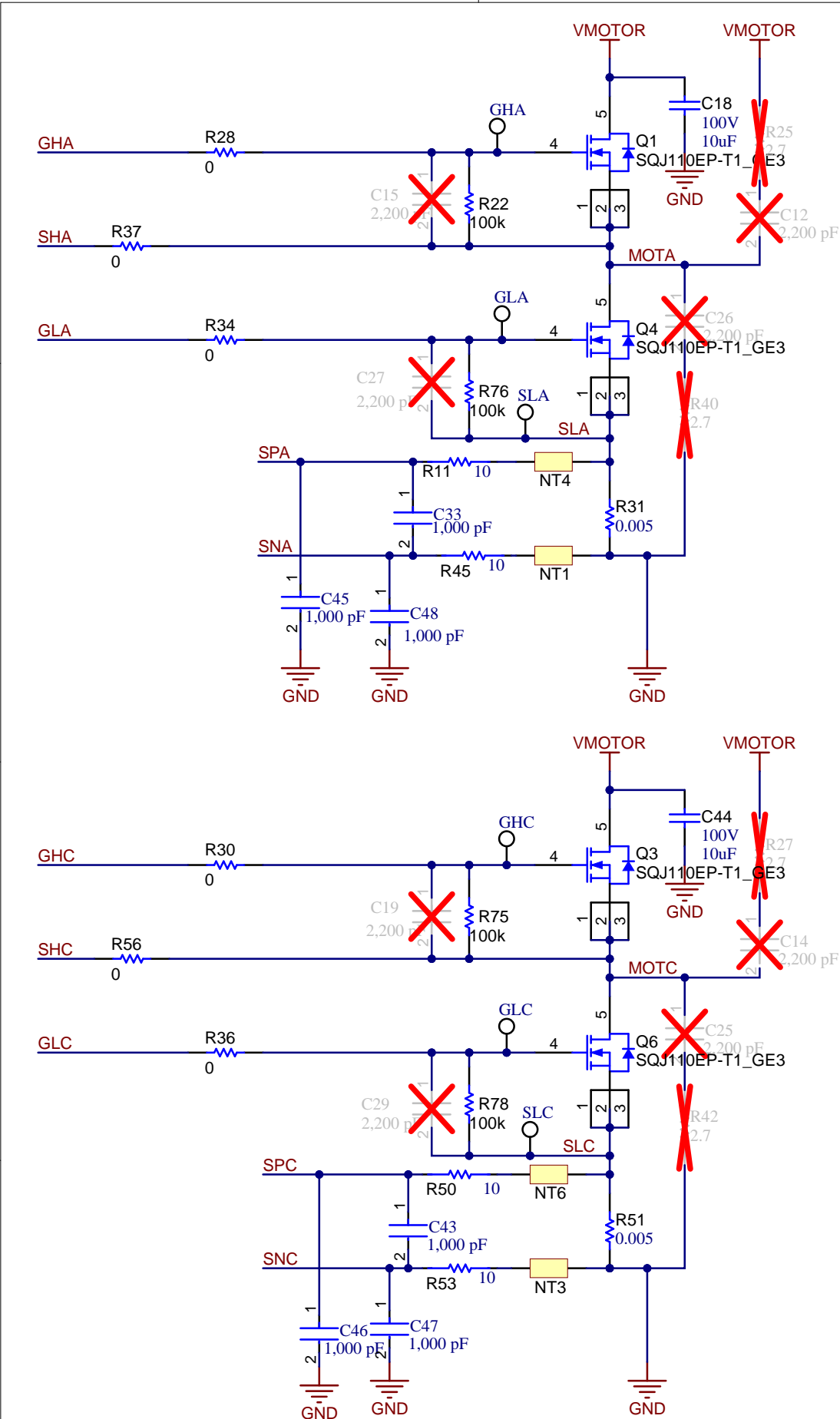
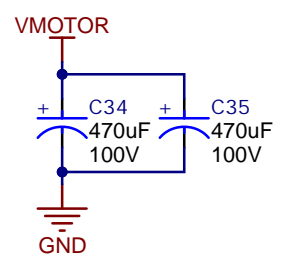


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Orderable: DRV8363EVM	Designed for: Public Release	Mod. Date: 4/3/2025
TID #: N/A	Project Title: DRV8363EVM	
Number: MD104	Rev:	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 5
Drawn By: Joshua Maize	File: MD104_Driver.SchDoc	Size: Letter
Engineer: Joshua Maize	Contact: http://www.ti.com/support	



When using single shunt mode, use Phase B sense resistor, populate these resistors and DNI Phase A and Phase C sense resistors



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Orderable: DRV8363EVM	Designed for: Public Release	Mod. Date: 4/8/2025
TID #: N/A	Project Title: DRV8363EVM	
Number: MD104	Rev: 001	Sheet: 2 of 5
SVN Rev: Not in version control	Assembly Variant: 001	Size: Letter
Drawn By: Joshua Maize	File: MD104_HalfBridges.SchDoc	http://www.ti.com
Engineer: Joshua Maize	Contact: http://www.ti.com/support	© Texas Instruments 2024

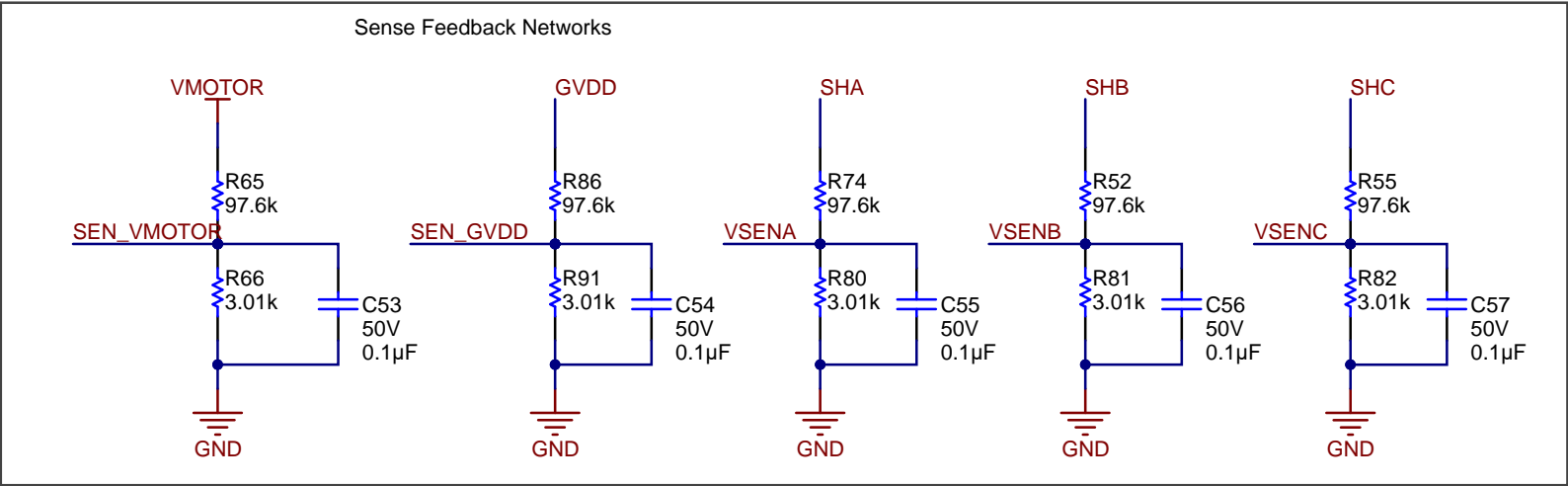
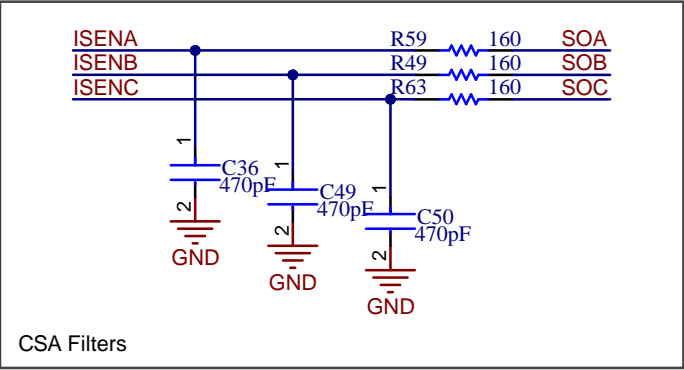
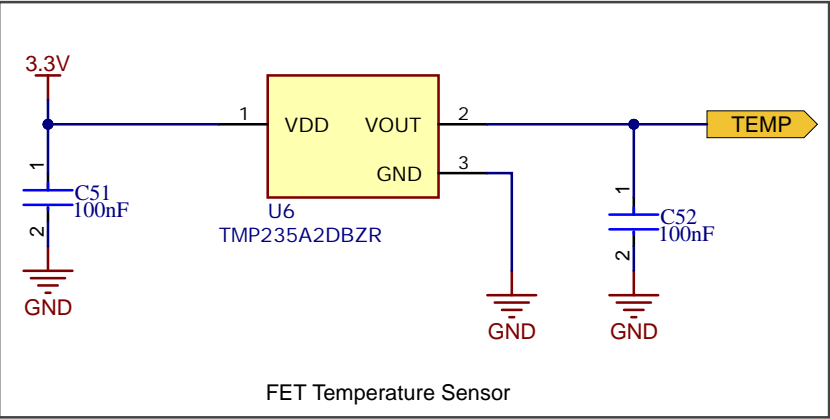


A

B

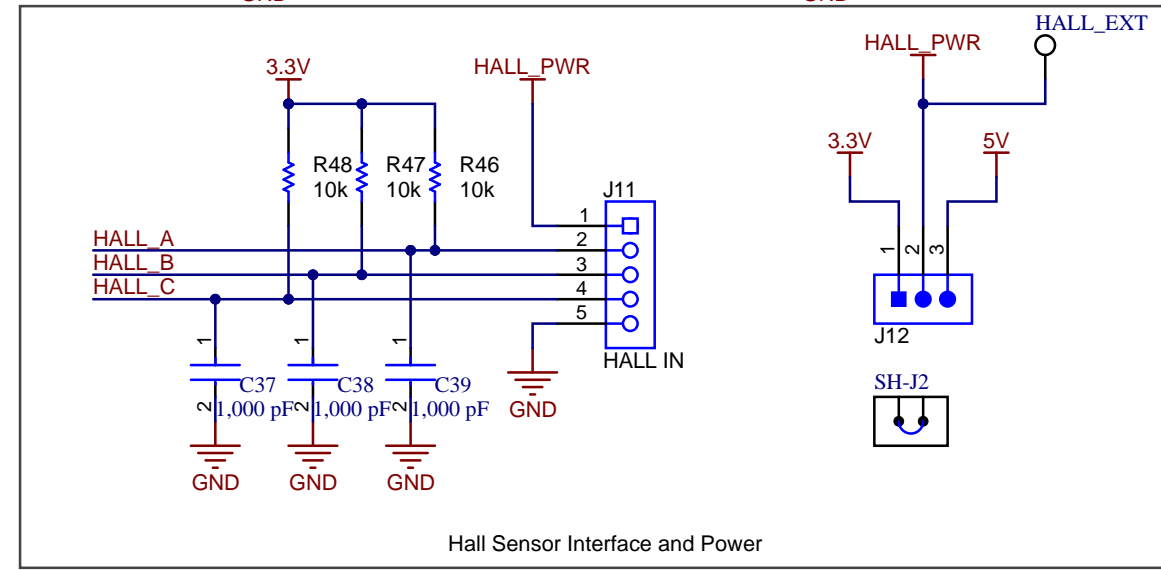
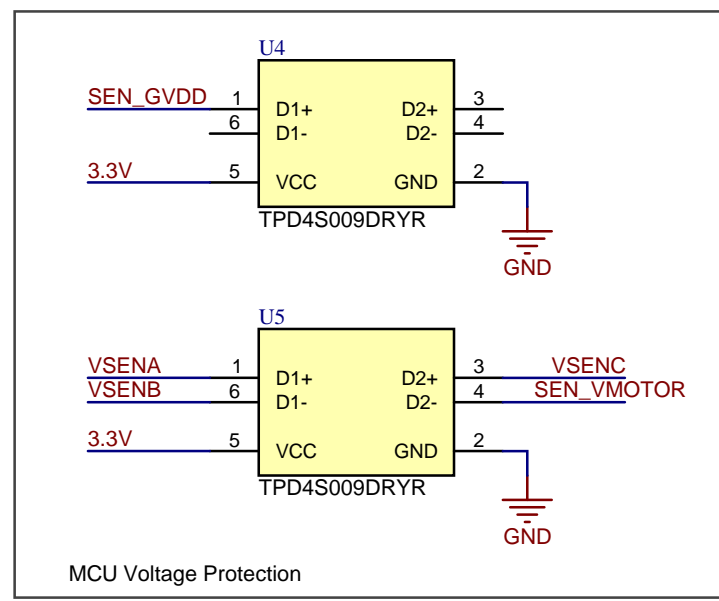
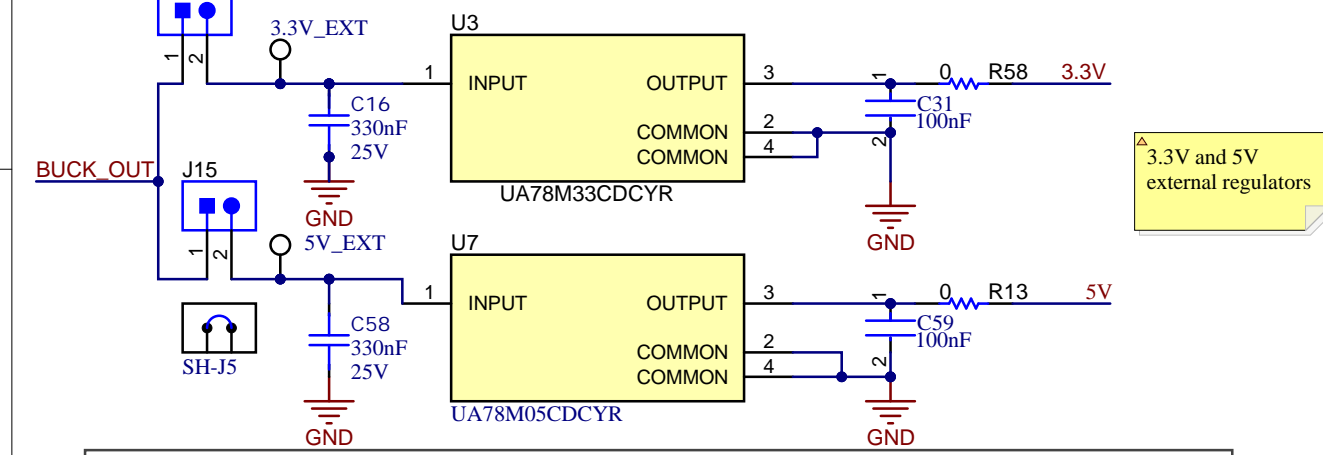
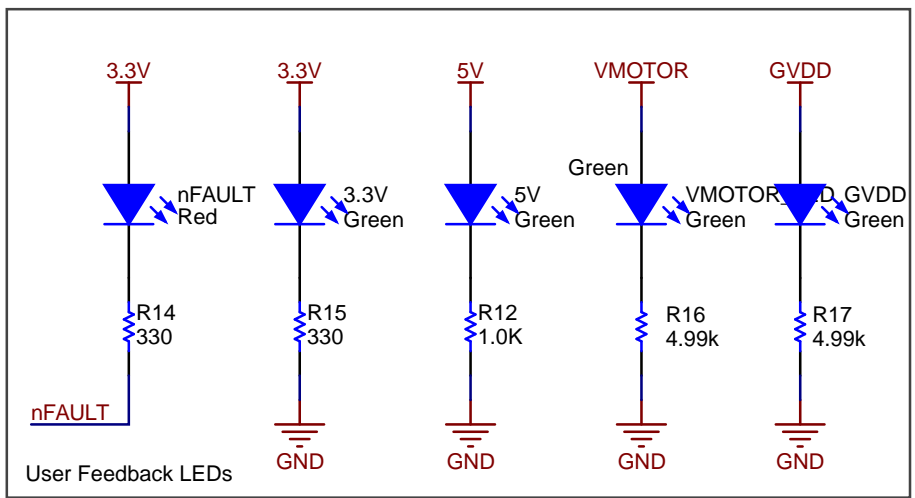
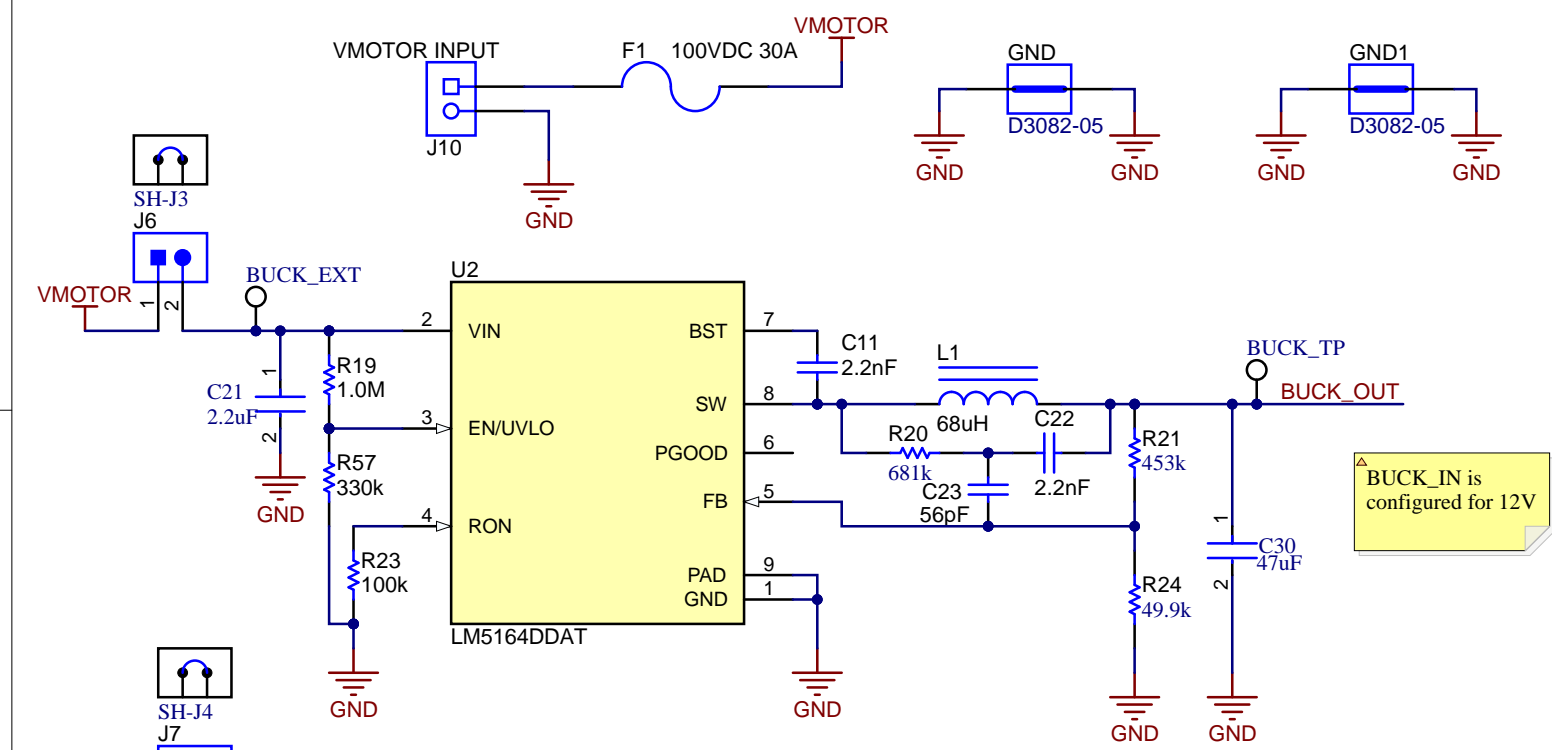
C

D



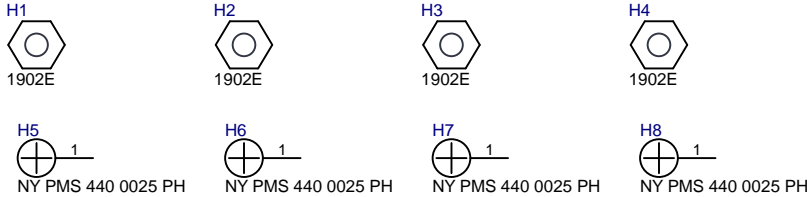
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Orderable: DRV8363EVM		Designed for: Public Release		Mod. Date: 2/12/2025	
TID #: N/A		Project Title: DRV8363EVM			
Number: MD104		Rev:		Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001		Sheet: 3 of 5	
Drawn By: Joshua Maize		File: MD104_SenseFeedback.SchDoc		Size: Letter	
Engineer: Joshua Maize		Contact: http://www.ti.com/support			




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Orderable: DRV8363EVM	Designed for: Public Release	Mod. Date: 4/8/2025
TID #: N/A	Project Title: DRV8363EVM	
Number: MD104	Rev:	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By: Joshua Maize	File: MD104_Power.SchDoc	Size: Letter
Engineer: Joshua Maize	Contact: http://www.ti.com/support	



PCB Number: MD104
PCB Rev:

PCB
LOGO
Texas Instruments



CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo



Variant/Label Table	
Variant	Label Text
001	DRV8363EVM

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.